



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Leonard Forbes et al. Examiner: Unknown  
Serial No.: 08/903,486 Group Art Unit: 1104  
Filed: July 29, 1997 Docket: 303.326US1  
Title: SILICON CARBIDE GATE TRANSISTOR AND FABRICATION PROCESS

**COMMUNICATION CONCERNING RELATED CO-PENDING APPLICATION**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Applicant would like to bring to the Examiner's attention the following related co-pending application for the above-identified patent application :

<u>Serial No.</u>	<u>Filing Date</u>	<u>Title</u>
08/958,023	October 27, 1997	GRADED ANTI-REFLECTIVE COATING FOR IC LITHOGRAPHY

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**AUG 20 1998**

**GROUP 2200**

Respectfully submitted,

LEONARD FORBES ET AL.

By their Representatives,

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Date

Aug. 14, 1998

By

Daniel J. Kluth

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Reg. No. 32,146

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Assistant Commissioner of Patents, Washington, D.C. 20231 on August 14, 1998.

Name

Daniel J. Kluth

Signature

Daniel J. Kluth



GAU 404

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We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ Communication Concerning Related CoPending Application (1 pg).

**Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.**

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this Transmittal Letter and the paper, as described above, are being deposited in the United States Postal Service, as first class mail, in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on this 14 day of August, 1998.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
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By: Daniel J. Kluth  
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Reg. No. 32,146

(GENERAL)